

Key Vocabulary	<i>Defensive design Authentication Validation Verification</i>	<i>Maintainability Indentation Commenting Whitespace Colour Testing Stepping Line numbers</i>	<i>Testing (iterative, final, terminal) Syntax, logic errors Normal, boundary, invalid, erroneous</i>	<i>Boolean AND OR NOT Truth table</i>	<i>Boolean AND OR NOT Truth table</i>	<i>Low level & high level languages Translators (interpreter, compiler)</i>	<i>IDE (integrated development environment) Diagnostic Run-time</i>	
Absenteeism / Isolation / Stretch & Challenge	<i>All resources and programming tutorials are located in Teams Files and within OneNote (Content Library / student section)</i>	<i>All resources and programming tutorials are located in Teams Files and within OneNote (Content Library / student section)</i>	<i>All resources and programming tutorials are located in Teams Files and within OneNote (Content Library / student section)</i>	<i>All resources and programming tutorials are located in Teams Files and within OneNote (Content Library / student section)</i>	<i>All resources and programming tutorials are located in Teams Files and within OneNote (Content Library / student section)</i>	<i>All resources and programming tutorials are located in Teams Files and within OneNote (Content Library / student section)</i>	<i>All resources and programming tutorials are located in Teams Files and within OneNote (Content Library / student section)</i>	
Recommended reading	<i>All resources (reading materials) and tutorials are located in Teams Files and class OneNote. Additional reading is posted in Teams general channel.</i> Craig 'n' Dave videos for SLR 2.3 Craig 'n' Dave videos for SLR 2.4 Craig 'n' Dave videos for SLR 2.5							
Knowledge Organiser Link	<i>Knowledge Organisers are located in the Content Library of the class OneNote Notebook</i>							

Curriculum Overview 2023/24: Term 2

Year: 12/13

Subject:	Learning Query / Topic: Systems architecture							
	30 Oct 2023	6 Nov 2023	13 Nov 2023	20 Nov 2023	27 Nov 2023	4 Dec 2023	11 Dec 2023	18 Dec 2023

Learning Question	<p>The arithmetic logic unit; ALU, Control Unit and Registers (Program Counter; PC, Accumulator; ACC, Memory Address Register; MAR, Memory Data Register; MDR, Current Instruction Register; CIR). Buses: data, address and control: How this relates to assembly language program.</p> <p>The fetch-decode-execute cycle, including its effect on registers.</p> <p>The factors affecting the performance of CPU, clock speed, number of cores, cache.</p> <p>Programming Project</p>	<p>The use of pipelining in a processor to improve efficiency. Von Neumann, Harvard and contemporary processor architecture. Programming Project</p>	<p>The differences between, and uses of, CISC and RISC processors GPUs and their uses (including those not related to graphics) Multicore and parallel systems Programming Project</p>	<p>How different input output and storage devices can be applied as a solution of different problems The uses of magnetic, flash and optical storage devices RAM and ROM Virtual storage Programming Project</p>	<p>The need for, function and purposes of operating systems Memory management (paging, segmentation and virtual memory) The role of interrupts and Interrupt Service Routines (ISR) within the fetch-decode-execute cycle Programming Project</p>	<p>Scheduling: round robin, first come first served, multi-level feedback queues, shortest job first and shortest remaining time Distributed, embedded, multi-tasking, multi-user and real-time operating systems BIOS Programming Project</p>	<p>Device drivers Virtual machines: any instance where software is used to take on the function of a machine including executing intermediate code or running an operating system within another operating system Programming Project</p>	<p>Programming Project</p>
Homework	<i>Smart Revise Project</i>	<i>Smart Revise Project</i>	<i>Smart Revise Project</i>	<i>Smart Revise Project</i>	<i>Smart Revise Project</i>	<i>Smart Revise Project</i>	<i>Smart Revise Project</i>	

Key Vocabulary	<i>Registers Cache Random Access Memory (RAM) Read Only Memory (ROM) Central Processing Unit (CPU) Arithmetic Logic Unit (ALU) Control Unit (CU) Accumulator (ACC)</i>	<i>Pipelining Processor Queueing</i>	<i>CISC (complex instruction set computers) RISC (reduced instruction set computers) GPU (graphics processing unit)</i>	<i>Input, output Flash, optical, magnetic storage devices Virtual memory</i>	<i>Operating systems Memory management (paging, segmentation, virtual) Interrupt service routines (ISR)</i>	<i>Scheduling: round robin, first come first served, multi-level feedback queues, shortest job first and shortest remaining time Basic input output system (BIOS)</i>	<i>Device driver Virtual machine</i>	
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